# **APPLICATION NOTE**





# **Automotive Load Dump Protection**



Modern automotive designs use a mass of electronics, such as control units, infotainment systems and sensors, all being powered by the car battery and the alternator. During the standard operation, several unwanted EMI disturbances arise from the various sources such as alternator, ignition systems, inductive load switching and load dump transient surges, which can be harmful to vehicle electronic devices. They can degrade performance, lead to malfunction of sensors or even permanently destroy electronic devices which could affect complete system's reliability.

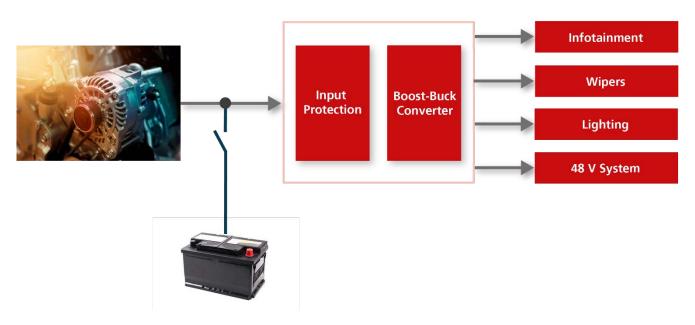
A major challenge out of these is to protect these electronics against the high energy load dump transient events, which is the most severe disturbance having large transient peaks, with duration of several milliseconds.

A load dump transient is the voltage surge generated when the battery is disconnected while the alternator is connected. This typically happens if a discharged car battery is powered up by a second one and afterwards jump leads are removed. The peak voltage of this surge may be as high as 120 V and the surge may take up to 400 ms to decay. It is typically clamped to 40 V in 12 V vehicles (cars) and about 60 V in 24 V systems (trucks).

Diotec offers various TVS product families for load dump protection according to ISO 16750-2:2012(E), both for applications without centralized load dump suppression (according to standard Table 5, Test A) and for those with centralized suppression (Table 6, Test B).

The new 6.6SM8Z series with 6.6 kW peak pulse power in DO-218AB is already available, it will soon be complemented by the 4.6SM6Z series with 4.6 kW. Both versions are qualified according to AEC-Q101. The 5KP represents an alternative in axial lead package, it allows up to 5 kW at the usual rating with 10/1000µs pulses. Classical alternator rectifier diodes in pressfit packages are the BYZ35 and BYZ50, which we offer especially for the after sales market. They offer a suppressor characteristic and thus provide alternator rectification and centralized suppression in one.

The following block diagram shows the typical current path from the alternator via battery to the stabilized DC voltage for the on-board electronics, including the option for a parallel 48 V system.



Block diagram for a modern automotive power supply circuit

#### **Disclaimer**

This application note describes device proposals and shall not be considered as assured and proven solution for any circuit. No warranty or guarantee, expressed or implied is made regarding the capacity, performance or suitability of any device, circuit etc.



#### Standards for the Protection of Automotive Electronics

#### ISO7637 and ISO 16750-2

This sequence of standards by the International Organization for Standardization is often considered as the "guideline" for automotive testing. ISO7637-2 and ISO 16750-2 were prepared by Technical Committee ISO/TC 22, Road vehicles, Subcommittee SC 3, Electric and electronic equipment. Both the standards describe the potential environmental stresses and specifies tests and requirements recommended for the specific mounting location on/in the road vehicle [1]

ISO 7637 stands for **Road vehicles-Electrical disturbances from conduction and coupling**, while the part 2 (ISO 7637-2) defined for the "Electrical transient conduction along supply lines only". It is a test standard which primarily designated for **EMC**. <sup>[1]</sup> In 2011, the power supply tests which are not related to EMC, such as Load DUMP test, which includes pulse 5a and 5b to ISO16750.

ISO 16750 stands for **Road vehicles-Environmental conditions and testing for electrical and electronic equipment**, while the part 2 (ISO 16750-2), focus on the Electrical loads. It is a standard which provides guidance on the events related to power supply guality. [1]

| Damanakana |                 | IS07637-2       |                        | ISO16750-2    |                |                         |
|------------|-----------------|-----------------|------------------------|---------------|----------------|-------------------------|
| Parameters | UN = 12 V       | UN = 24 V       | Min. Test requirements | UN = 12 V     | UN = 24 V      | Min. Test requirements  |
| Us [V]     | 65 ≤ Us ≤ 87    | 123 ≤ Us ≤ 174  |                        | 79 ≤ Us ≤ 101 | 151 ≤ Us ≤ 202 |                         |
| Us* [V]    | Defined by user | Defined by user |                        | 35            | 65             | 10 pulses at            |
| UA [V]     | 13-14           | 26-28           | 1 pulso                | 14            | 28             |                         |
| Ri [ohm]   | 0.5 ≤ Ri ≤ 4    | 1 ≤ Ri ≤ 8      | 1 pulse                | 0.5 ≤ Ri ≤ 4  | 1 ≤ Ri ≤ 8     | interval of 1<br>minute |
| Td [ms]    | 40 ≤ td ≤ 400   | 100 ≤ td ≤ 350  |                        | 40 ≤ td ≤ 400 | 100 ≤ td ≤ 350 |                         |
| Tr [ms]    | 10 +0/-5        | 10 +0/-5        |                        | 10 +0/-5      | 10 +0/-5       |                         |

Table 1: Pulses for Test A/Test B in systems with 12 V and 24 V nominal voltage

For simplicity, the further explanations focus on testing parts according to the standard ISO16750-2 and 12V systems (cars).

Alternators in the conventional automobiles could produce a large voltage surge during load dump, about 100V at 12V systems. Modern automobiles are already embedded with alternators providing additional clamping features, to limit the voltage surges. Having thus two different generations of alternators, ISO 16750-2 specifies load dump waveforms by Test A and Test B accordingly:

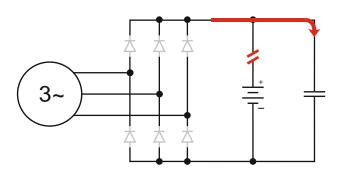
Test A – without centralized load dump suppression

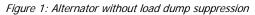
Test B – with centralized load dump suppression



#### Test A - Without Centralized Load Dump Suppression

Figure 1. shows the high voltage load dump transient surge appearing when the battery is being disconnected while the alternator continues supplying power. Figure 1.1 shows the related unclamped waveform, described by Test A in ISO16750-2.





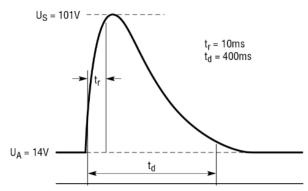


Figure 1.1: Pulse Test A – unclamped load dump

#### Test B - With Centralized Load Dump Suppression

Figure 2 shows the alternator output voltage with centralized load dump suppression, e. g. by rectifier diodes with clamping characteristic. Figure 2.1 shows the clamped waveform described by Test B in ISO16750-2.

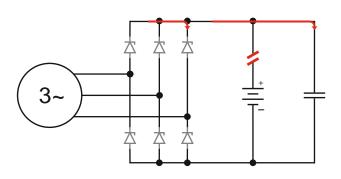


Figure 2: Alternator with load dump suppression

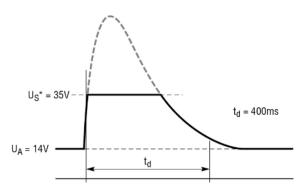


Figure 2.1: Pulse Test B – clamped load dump



# Load Dump Protection Diodes passing Test A and Test B

# ► Components to provide a <u>Centralized Load Dump Suppression!</u>

Protection only (12 V system | 24 V system)

| Part No.                         | Package     | P <sub>PPM</sub>   | V <sub>wm</sub> | V <sub>BR</sub> |
|----------------------------------|-------------|--------------------|-----------------|-----------------|
| 6.6SM8Z /<br>4.6SM8Z<br>AEC-Q101 | J DO. 218AB | 6600 W /<br>4600 W | 10 40 V<br>43 V | 11 47 V<br>51 V |

| Part No.                     | Package   | P <sub>PPM</sub>   | V <sub>WM</sub> |                    | $V_{BR}$           |
|------------------------------|-----------|--------------------|-----------------|--------------------|--------------------|
| LDP01 /<br>LDP02<br>AEC-Q101 | J. D. PAK | 5000 W /<br>6600 W |                 | 22 40 V<br>43 70 V | 26 47 V<br>51 82 V |

| Part No.                         | Package: D8 x 7.5   | P <sub>PPM</sub> | V <sub>WM</sub>    | $V_{BR}$           |
|----------------------------------|---|------------------|--------------------|--------------------|
| <b>5KP</b> Qualification planned | E SCHOOL | 5000 W           | 28 40 V<br>48 60 V | 33 47 V<br>56 68 V |

# <u>Protection and Rectification = Protectifiers</u>®

# (12 V system | 24 V system)

| Part No.                                      | Package: D12.75 x 4.2 / Press-fit | I <sub>FAV</sub> | V <sub>WM</sub>            | $V_{BR}$           |
|---|-----------------------------------|------------------|----------------------------|--------------------|
| BYZ35 /<br>BYZ50<br>For after<br>sales market | J. Protection                     | 35 A* /<br>50 A* | 17.8 26.8 V<br>31.6 38.1 V | 22 33 V<br>39 47 V |



# Load Dump Protection Diodes passing Test B

# ► Components to be used at Centralized Load Dump Suppression Systems!

Centralized load dump suppression limits the voltage surge to 35 V (12 V system) respectively 58 V (24 V system). In consequence, TVS diodes with a clamping voltage >> 35 V (58 V) can be used to protect the on-board electronics. The maximum level of clamping voltage is determined by the ability of the circuits to be protected.

| Part No.                     | Package DO-213AA | РРРМ  | V <sub>WM</sub>            | V <sub>BR</sub>    |
|------------------------------|------------------|-------|----------------------------|--------------------|
| TGL34  Qualification planned | J. William       | 150 W | 28.2 40.2 V<br>47.8 58.1 V | 33 47 V<br>56 68 V |

| Part No.                        | Package DO-213AB | P <sub>PPM</sub> | V <sub>WM</sub>            | $V_{BR}$           |
|---------------------------------|------------------|------------------|----------------------------|--------------------|
| TGL41 TGL61 (under development) | S will be        | 400 W<br>600 W   | 28.2 40.2 V<br>47.8 58.1 V | 33 47 V<br>56 68 V |
| Qualification planned           | <b>3</b>         |                  |                            |                    |

| Part No.              | Package: SOD-123FL | P <sub>PPM</sub> | V <sub>WM</sub> | V <sub>BR</sub> |
|-----------------------|--------------------|------------------|-----------------|-----------------|
| SMF                   | ///JC 80           |                  | 28 40 V         | 33 47 V         |
| Qualification planned | 1235               | 200 W            | 48 60 V         | 56 68 V         |

| Part No.           | Package DO-214AC | P <sub>PPM</sub> | V <sub>WM</sub>    | V <sub>BR</sub>    |
|--------------------|------------------|------------------|--------------------|--------------------|
| P4SMAJ<br>AEC-Q101 | J'SMA            | 400 W            | 28 40 V<br>48 60 V | 33 47 V<br>56 68 V |

| Part No.         | Package: SMAF | P <sub>PPM</sub> | V <sub>WM</sub>    | V <sub>BR</sub>    |
|------------------|---------------|------------------|--------------------|--------------------|
| TPSMA6L AEC-Q101 | J'SMAF        | 600 W            | 28 40 V<br>48 60 V | 33 47 V<br>56 68 V |



## Load Dump Protection Diodes passing Test B

## ► Components to be used at Centralized Load Dump Suppression Systems!

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| Part No.                                    | Package DO-214AA | P <sub>PPM</sub> | V <sub>WM</sub>    | V <sub>BR</sub>    |
|---|------------------|------------------|--------------------|--------------------|
| P6SMBJ 1.0SMBJ (under development) AEC-Q101 | J'SMB            | 600 W<br>1000 W  | 28 40 V<br>48 60 V | 33 47 V<br>56 68 V |

| Part No.                | Package DO-214AB | P <sub>PPM</sub> | V <sub>WM</sub>    | V <sub>BR</sub>    |
|-------------------------|------------------|------------------|--------------------|--------------------|
| <b>1.5SMCJ</b> AEC-Q101 | J'sMC            | 1500 W           | 28 40 V<br>48 60 V | 33 47 V<br>56 68 V |

| Part No.                | Package DO-214AB | P <sub>PPM</sub> | V <sub>wm</sub>    | $V_{BR}$           |
|-------------------------|------------------|------------------|--------------------|--------------------|
| <b>3.0SMCJ</b> AEC-Q101 | J'sMC            | 3000 W           | 28 40 V<br>48 60 V | 33 47 V<br>56 68 V |

| Part No.                | Package DO-214AB | P <sub>PPM</sub> | V <sub>WM</sub>    | V <sub>BR</sub>    |
|-------------------------|------------------|------------------|--------------------|--------------------|
| <b>5.0SMCJ</b> AEC-Q101 | I smc            | 5000 W           | 28 40 V<br>48 60 V | 33 47 V<br>56 68 V |

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